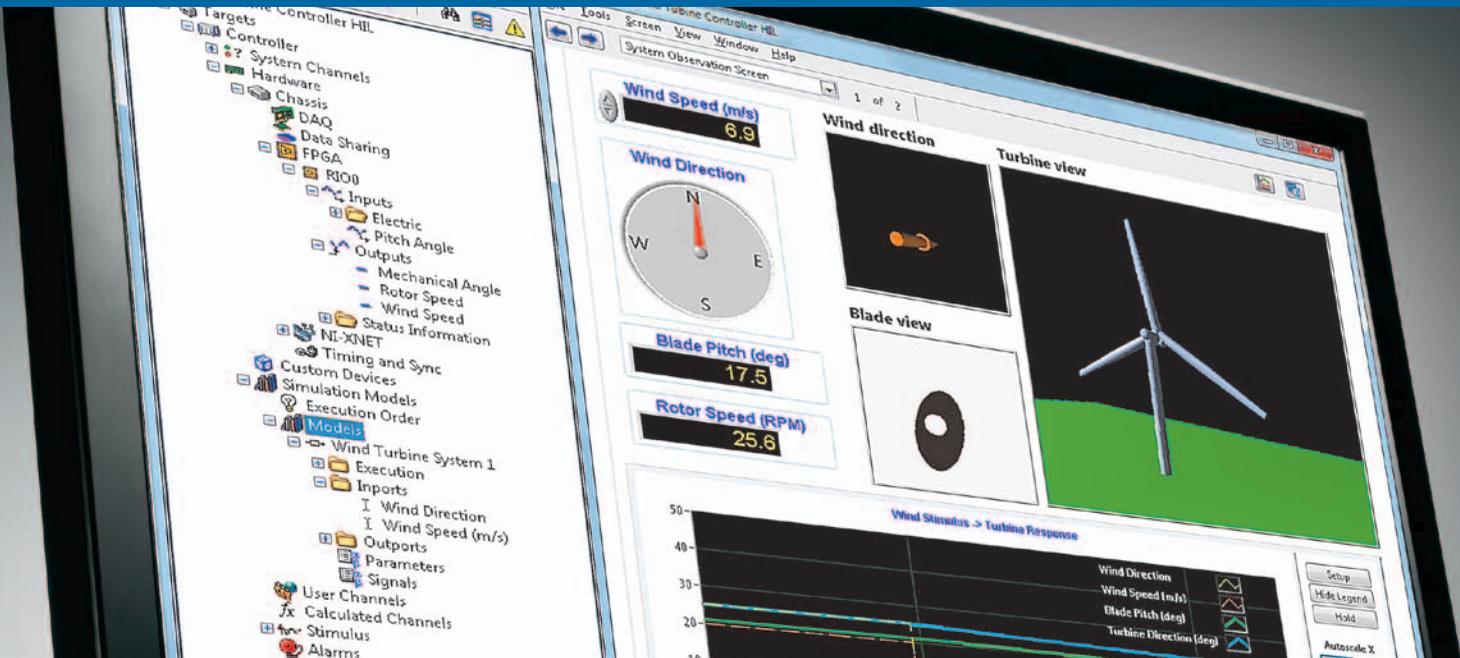


NI VeriStand

Real-Time Testing

Model-Based Simulation

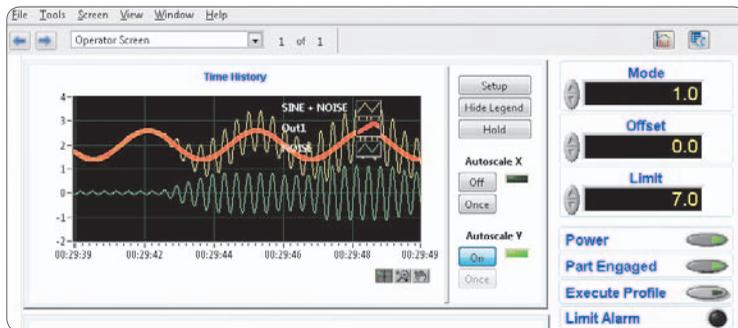
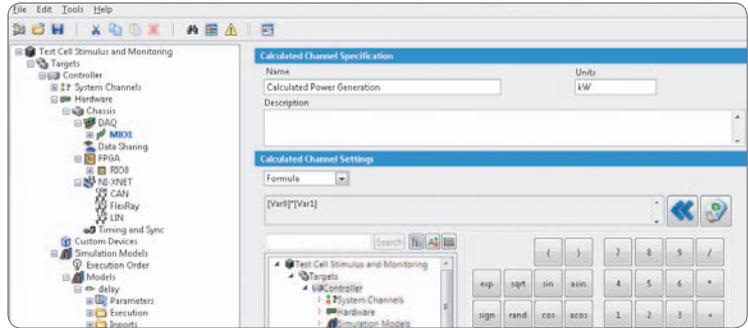
Closed-Loop Control



NI VeriStand is an open software environment for configuring real-time testing applications. Its out-of-the-box functionality gets you up and running quickly so you can reduce your development cost. No programming knowledge is necessary; however, the ability to add application-specific functionality using NI LabVIEW, C/C++, and other environments ensures the flexibility and security of an open development platform.

Multicore-Enabled Real-Time Engine

- Configure real-time hardware I/O interfaces including NI FPGA-based RIO hardware
- Detect and respond to events with configurable alarms and procedures
- Connect simulation models and closed-loop control algorithms to I/O and other real-time tasks

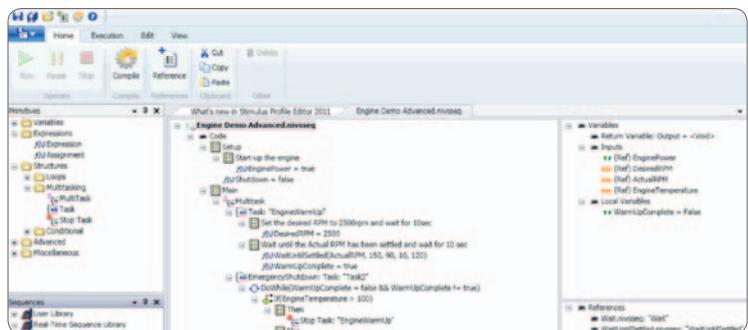


Run-Time Editable User Interface

- Monitor application data, alarm states, and system execution metrics
- Enable test automation with macro recording or with NI TestStand, .NET, and other software
- Regulate operator access levels with the user management utility

Real-Time Stimulus and Data Logging

- Generate stimulus signals from recorded data or other software environment profiles
- Use branching and looping steps to implement sophisticated test sequences
- Configure data-logging tasks with independent logging rates, channel sets, and trigger conditions



Configure real-time I/O interfaces including

- Analog/digital
- FPGA-based
- Strain/load/torque
- Temperature
- Fault insertion
- Automotive buses
- Aerospace buses
- Reflective memory

Import control algorithms and simulation models including

- NI LabVIEW
- C/C++ /FORTRAN/Ada
- AVL BOOST/CRUISE
- The MathWorks, Inc. Simulink® software
- SimulationX from ITI
- MapleSim from Maplesoft
- GT-POWER from GTI

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